

## **Res. Asst. Latif Akçay**

### **Personal Information**

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**Address:** İstanbul Teknik Üniversitesi, Ayazağa Kampüsü, Elektrik-Elektronik Fakültesi, Oda:1115, SARIYER/İSTANBUL

### **International Researcher IDs**

ORCID: 0000-0003-2580-2643

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### **Education Information**

Doctorate, İstanbul Technical University, Elektrik - Elektronik Fakültesi, Elektronik Ve Haberleşme Mühendisliği, Turkey  
2016 - Continues

Postgraduate, İstanbul Technical University, Elektrik - Elektronik Fakültesi, Elektronik Ve Haberleşme Mühendisliği,  
Turkey 2014 - 2016

Undergraduate, Bülent Ecevit Üniversitesi, Elektrik - Elektronik Fakültesi, Turkey 2008 - 2013

### **Foreign Languages**

English, B2 Upper Intermediate

### **Certificates, Courses and Trainings**

IT, C programlama dili, C ve Sistem Programcılar Derneği, 2016

### **Dissertations**

Postgraduate, Açık Kaynak Kodlu OpenRISC Tabanlı Kirmiküstü Sistemlerin Gerçeklenmesi ve Uygulamaları, İstanbul  
Teknik Üniversitesi, Elektrik Elektronik Fakültesi, Elektronik Ve Haberleşme Mühendisliği, 2016

### **Research Areas**

Electrical and Electronics Engineering, Electronic, Electronic Circuits, Engineering and Technology

### **Academic Titles / Tasks**

Research Assistant, İstanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, 2015 -  
2022

Research Assistant, Bayburt University, Faculty Of Engineering, Department Of Electrical And Electronics Engineering,  
2014 - 2015

## **Refereed Congress / Symposium Publications in Proceedings**

- I. **Implementation of a chaotic time-delay RNG based secure communication system on FPGA**  
Akçay L., Cil E., Vardar A., Yaman I., Yeniçeri R., Yalçın M. E.  
10th International Conference on Electrical and Electronics Engineering, ELECO 2017, Bursa, Turkey, 29 November - 02 December 2017, pp.1277-1280
- II. **A Chaotic Time-delay System Based Digital RNG and Integrated Autonomous Test Suite**  
Yeniçeri R., Vardar A., Çil E., Akçay L., Göncü E., Yalçın M. E.  
European Conference on Circuit Theory and Design 2017 (ECCTD 2017), Catania, Italy, 4 - 06 September 2017, pp.1-4
- III. **Design and Implementation of an OpenRISC System-on-Chip with an Encryption Peripheral**  
Akçay L., Tukel M., Ors B.  
European Conference on Circuit Theory and Design (ECCTD), Catania, Italy, 4 - 06 September 2017
- IV. **Implementation of a Chaotic Time-Delay RNG Based Secure Communication System on FPGA**  
Akçay L., Cil E., Vardar A., Yaman I., Yeniçeri R., Yalcin M. E.  
10th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 30 November - 02 December 2017, pp.1277-1280
- V. **Implementation of an OpenRISC based SoC and Linux Kernel installation on FPGA**  
Akçay L.  
Sinyal İşleme Uygulamaları, Zonguldak, Turkey, 16 - 19 May 2016, pp.1-4
- VI. **Implementation of an OpenRISC based SoC and Linux Kernel installation on FPGA OpenRISC Tabanlı Bir Kirmiküstü Sistemin FPGA Üzerinde Gerçeklenmesi ve Linux Kernel Kurulumu**  
Akçay L., TUCEL M., Ors S. B.  
24th Signal Processing and Communication Application Conference, SIU 2016, Zonguldak, Turkey, 16 - 19 May 2016, pp.1969-1972

## **Metrics**

Publication: 8

Citation (WoS): 4

Citation (Scopus): 4

H-Index (WoS): 1

H-Index (Scopus): 2

## **Non Academic Experience**

ANKASYS MİKROELEKTRONİK SİSTEMLER AŞ