



## **Personal Information**

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## **International Researcher IDs**

**ORCID:** 0000-0002-6921-8142

**Publons / Web Of Science ResearcherID:** AAV-1469-2021

**ScopusID:** 57197799614

**Yoksis Researcher ID:** 226539

## **Education Information**

**Doctorate,** İstanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, Turkey 2017 - Continues

**Postgraduate,** İstanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, Turkey 2014 - 2017

**Undergraduate,** İstanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, Turkey 2008 - 2014

## **Foreign Languages**

Italian, A1 Beginner

English, C1 Advanced

## **Dissertations**

**Postgraduate,** OTOMATİK KONTROL SİSTEMLERİ İÇİN KAZANCI AYARLANABİLEN KUVVETLENDİRİCİ TASARIMI, İstanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, 2017

## **Research Areas**

Electrical and Electronics Engineering, Electronic, Sensing Devices and Transducers, Electronic Circuits, Microwave Circuits, Nanotechnology, Optics and Photonics

## **Published journal articles indexed by SCI, SSCI, and AHCI**

### **I. A switchable DC offset cancellation circuit for time-based degradation correction**

Erol D., Güngördü A. D., Dundar G., Yelten M. B.

ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, vol.106, no.3, pp.485-491, 2021 (SCI-Expanded)

### **II. An Automated Setup for the Characterization of Time-Based Degradation Effects Including the**

- Process Variability in 40-nm CMOS Transistors**  
 Xhafa X., Güngördü A. D., Erol D., Yavuz Y., Yelten M. B.  
 IEEE TRANSACTIONS ON INSTRUMENTATION AND MEASUREMENT, vol.70, 2021 (SCI-Expanded)
- III. Design of a constant-bandwidth variable-gain amplifier for LTE receivers**  
 Güngördü A. D., Tarim N.  
 ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, vol.97, no.1, pp.27-38, 2018 (SCI-Expanded)
- IV. Low input resistance current buffer stage using a controllable positive feedback loop, and applications of current conveyor based filters**  
 Güngördü A. D., Altun M., Çevik İ.  
 AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, vol.82, pp.58-65, 2017 (SCI-Expanded)

## Refereed Congress / Symposium Publications in Proceedings

- I. 40 nm CMOS Sureci Transistorlerinin Akım-Gerilim Karakteristiklerinin Oda Sıcaklığında ve Kriyojenik Koşullarda İncelenmesi**  
 Gencer F. B., İLİK S., GÜNGÖRDÜ A. D., Xhafa X., YELTEN M. B.  
 ELEKTRİK-ELEKTRONİK ve BİYOMEDİKAL MÜHENDİSLİĞİ KONFERANSI (ELECO), Bursa, Turkey, 24 - 26 November 2022
- II. A Noise-Canceling TIA Topology Compatible With Large-Area Photodetectors in 40 nm CMOS Process**  
 GÜNGÖRDÜ A. D., YELTEN M. B.  
 IEEE 65th International Midwest Symposium on Circuits and Systems (MWSCAS), Fukuoka, Japan, 7 - 10 August 2022, pp.1-4
- III. Ultra Low Power Transimpedance Amplifier Design for Receivers with Large-Area Photodetectors**  
 GÜNGÖRDÜ A. D., YELTEN M. B.  
 2021 13th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 25 - 27 November 2021, pp.98-101
- IV. A Phase Error Correction Algorithm for RF Energy Harvesters Using Two Antennas**  
 GÜNGÖRDÜ A. D., EROL D., ÇAĞLAR A., YELTEN M. B.  
 International Conference on Synthesis, Modeling, Analysis and Simulation Methods, and Applications to Circuit Design, Erfurt, Germany, 19 - 21 July 2021, pp.100-103
- V. A High Performance TIA Design in 40 nm CMOS**  
 GÜNGÖRDÜ A. D., DÜNDAR G., YELTEN M. B.  
 2020 IEEE International Symposium on Circuits and Systems (ISCAS), Sevilla, Spain, 12 - 14 October 2020
- VI. An Offset Cancellation Set-up for Amplifiers Subject to Aging**  
 Erol D., GÜNGÖRDÜ A. D., DÜNDAR G., YELTEN M. B.  
 11th International Conference on Electrical and Electronics Engineering, 28 - 30 November 2019
- VII. Analog Neural Network based on Memristor Crossbar Arrays**  
 Yıldız H. K., Altun M., Güngördü A. D., Stan M. R.  
 11th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 28 - 30 November 2019, pp.358-361
- VIII. A Novel CMOS Constant-Bandwidth Variable-Gain Amplifier for WiMAX Receivers**  
 Güngördü A. D., Tarim N.  
 10th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 30 November - 02 December 2017, pp.481-484

## Supported Projects

Yelten M. B., Karabulut Kurt G. Z., Güngördü A. D., Project Supported by Higher Education Institutions, Kablosuz Enerji

## Metrics

Publication: 14

Citation (WoS): 8

Citation (Scopus): 15

H-Index (WoS): 2

H-Index (Scopus): 2