

## Assoc. Prof. Mustafa Berke Yelten

### Personal Information

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### Education Information

Doctorate, North Carolina State University, Elektronik Mühendisliği, United States Of America 2008 - 2011

Post Graduate, North Carolina State University, Elektronik Mühendisliği, United States Of America 2006 - 2008

Under Graduate, Boğaziçi Üniversitesi, Mühendislik Fakültesi, Elektrik-Elektronik Mühendisliği Bölümü, Turkey 2003 - 2006

### Foreign Languages

English, C1 Advanced

German, C1 Advanced

### Dissertations

Doctorate, Variability and reliability in nanoscale circuits: Simulation, design, monitoring, and characterization, North Carolina State University, Elektronik Mühendisliği, 2011

Post Graduate, Theoretical analysis and design methodologies for low noise amplifiers based on tunable matching networks, North Carolina State University, Elektronik Mühendisliği, 2008

### Research Areas

Electrical and Electronics Engineering, Electronic, Electronic Circuits, Microwave Circuits, Nanotechnology, Engineering and Technology

### Academic Titles / Tasks

Associate Professor, Istanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, 2020 - Continues

Assistant Professor, Istanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, 2018 - 2020

Assistant Professor, Istanbul Technical University, Elektrik-Elektronik, Elektronik Ve Haberleşme Mühendisliği, 2015 - 2018

### Academic and Administrative Experience

Deputy Head of Department, Istanbul Technical University, 2020 - Continues  
Vice Dean, Istanbul Technical University, 2018 - 2020  
Fakülte Akademik Kurul Üyesi, Istanbul Technical University, 2015 - 2020

## Courses

Stokastik Modelleme, Under Graduate, 2017 - 2018  
RF Microelectronics, Under Graduate, 2017 - 2018  
Semiconductor Devices, Under Graduate, 2017 - 2018  
Analog Electronic Circuits, Under Graduate, 2017 - 2018  
Modeling of Semiconductor Devices and Components, Under Graduate, 2017 - 2018  
Microelectronics Technology, Under Graduate, 2016 - 2017

## Advising Theses

Yelten M. B. , A CRYOGENIC CMOS LOW DROPOUT REGULATOR DESIGN FOR SPACE APPLICATIONS, Post Graduate, H.İBRAHİM(Student), 2019  
Yelten M. B. , Radiation-induced effects on N-type metal oxide semiconductor field effect transistor devices: Modeling, simulation and optimization, Post Graduate, S.İLİK(Student), 2019  
Yelten M. B. , Statistical cryogenic modeling methodology of MOSFET DC characteristics in BSIM3, Post Graduate, A.KABAOĞLU(Student), 2019  
Yelten M. B. , A high linearity s-band cryogenic low noise amplifier using 180 nm CMOS technology for space applications, Post Graduate, A.ÇAĞLAR(Student), 2019  
Yelten M. B. , KÜP UYDULAR İÇİN, VHF/UHF BANDI AKTARICI TASARIMI VE GERÇEKLEMESİ, Post Graduate, H.Onur(Student), 2018  
Yelten M. B. , A 0.18  $\mu\text{m}$  CMOS X-BAND LOW NOISE AMPLIFIER FOR SPACE APPLICATIONS, Post Graduate, N.Şahin(Student), 2017

## Articles Published in Journals That Entered SCI, SSCI and AHCI Indexes

- I. **Monitoring modal shape of miniaturized dynamic structures via laser triangulation and stroboscopy**  
Erdem Y. E. , Yelten M. B. , Ferhanoğlu O.  
MICROSYSTEM TECHNOLOGIES-MICRO-AND NANOSYSTEMS-INFORMATION STORAGE AND PROCESSING SYSTEMS, 2021 (Journal Indexed in SCI)
- II. **Design of a tunable LNA and its variability analysis through surrogate modeling**  
Xhafa X., Yelten M. B.  
INTERNATIONAL JOURNAL OF NUMERICAL MODELLING-ELECTRONIC NETWORKS DEVICES AND FIELDS, vol.33, no.6, 2020 (Journal Indexed in SCI)
- III. **A wide-temperature range (77-400 K) CMOS low-dropout voltage regulator system**  
Kayihan H. I. , Inam B. B. , Dogan B., Yelten M. B.  
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, 2020 (Journal Indexed in SCI)
- IV. **Design and validation of an artificial neural network based on analog circuits**  
Gencer F. B. , Xhafa X., Inam B. B. , Yelten M. B.  
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, 2020 (Journal Indexed in SCI)
- V. **A switchable DC offset cancellation circuit for time-based degradation correction**  
Erol D., Güngördü A. D. , Dundar G., Yelten M. B.  
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, 2020 (Journal Indexed in SCI)
- VI. **Experimental and modeling studies of automotive-qualified OLEDs under electrical stress**

- Guney A., Yelten M. B. , Ferhanoğlu O., KAHRAMAN N.  
MICROELECTRONICS RELIABILITY, vol.111, 2020 (Journal Indexed in SCI)
- VII. **A 180-nm X-Band Cryogenic CMOS LNA**  
Caglar A., Yelten M. B.  
IEEE MICROWAVE AND WIRELESS COMPONENTS LETTERS, vol.30, no.4, pp.395-398, 2020 (Journal Indexed in SCI)
- VIII. **Reliability Testing of 3D-Printed Polyamide Actuators**  
Kasap G., Gokdel Y. D. , Yelten M. B. , Ferhanoğlu O.  
IEEE TRANSACTIONS ON DEVICE AND MATERIALS RELIABILITY, vol.20, no.1, pp.152-156, 2020 (Journal Indexed in SCI)
- IX. **Design of Cryogenic LNAs for High Linearity in Space Applications**  
Caglar A., Yelten M. B.  
IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS I-REGULAR PAPERS, vol.66, no.12, pp.4619-4627, 2019 (Journal Indexed in SCI)
- X. **Variability-aware cryogenic models of mosfets: validation and circuit design**  
Kabaoglu A., Sahin-Solmaz N., Ilik S., Uzun Y., Yelten M. B.  
SEMICONDUCTOR SCIENCE AND TECHNOLOGY, vol.34, no.11, 2019 (Journal Indexed in SCI)
- XI. **Modeling of Total Ionizing Dose Degradation on 180-nm n-MOSFETs Using BSIM3**  
İlik S., Kabaoglu A., Solmaz N. S. , Yelten M. B.  
IEEE TRANSACTIONS ON ELECTRON DEVICES, vol.66, no.11, pp.4617-4622, 2019 (Journal Indexed in SCI)
- XII. **On Chip Reconfigurable CMOS Analog Circuit Design and Automation Against Aging Phenomena: Sense and React**  
AFACAN E., DÜNDAR G., Baskaya F., PUSANE A. E. , Yelten M. B.  
ACM TRANSACTIONS ON DESIGN AUTOMATION OF ELECTRONIC SYSTEMS, vol.24, no.4, 2019 (Journal Indexed in SCI)
- XIII. **Radiation tolerance impact of trap density near the drain and source regions of a MOSFET**  
İlik S., Gencer F. B. , Solmaz N. S. , Çağlar A., Yelten M. B.  
NUCLEAR INSTRUMENTS & METHODS IN PHYSICS RESEARCH SECTION B-BEAM INTERACTIONS WITH MATERIALS AND ATOMS, vol.449, pp.1-5, 2019 (Journal Indexed in SCI)
- XIV. **Silver nanowire coated knitted wool fabrics for wearable electronic applications**  
Gürarlan A., Ozdemir B., Bayat I. H. , Yelten M. B. , Karabulut Kurt G. Z.  
JOURNAL OF ENGINEERED FIBERS AND FABRICS, vol.14, 2019 (Journal Indexed in SCI)
- XV. **Statistical MOSFET Modeling Methodology for Cryogenic Conditions**  
KABAOĞLU A., Solmaz N. S. , İlik S., UZUN Y., Yelten M. B.  
IEEE TRANSACTIONS ON ELECTRON DEVICES, vol.66, no.1, pp.66-72, 2019 (Journal Indexed in SCI)
- XVI. **Time-dependent dielectric breakdown (TDDB) reliability analysis of CMOS analog and radio frequency (RF) circuits**  
SANIÇ M. T. , Yelten M. B.  
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, vol.97, no.1, pp.39-47, 2018 (Journal Indexed in SCI)
- XVII. **Surrogate modeling and variability analysis of on-chip spiral inductors**  
AKSO E., SOYSAL I. B. , Yelten M. B.  
INTERNATIONAL JOURNAL OF NUMERICAL MODELLING-ELECTRONIC NETWORKS DEVICES AND FIELDS, vol.31, no.5, 2018 (Journal Indexed in SCI)
- XVIII. **Reliability Testing of 3D-printed Electromechanical Scanning Devices**  
Gönültaş B. M. , SAVAŞ J., Khayat-zadeh R., Aygün S., ÇİVİTÇİ F., GÖKDEL Y. D. , YELTEN M. B. , FERHANOĞLU O.  
Journal Of Electronic Testing-Theory And Applications, 2018 (Journal Indexed in SCI Expanded)
- XIX. **Aging signature properties and an efficient signature determination tool for online monitoring**  
Afacan E., Dunder G., Pusane A. E. , Yelten M. B. , Baskaya F.  
INTEGRATION-THE VLSI JOURNAL, vol.58, pp.496-503, 2017 (Journal Indexed in SCI)
- XX. **Comparison of modeling techniques in circuit variability analysis**  
Yelten M. B. , Franzon P. D. , Steer M. B.

INTERNATIONAL JOURNAL OF NUMERICAL MODELLING-ELECTRONIC NETWORKS DEVICES AND FIELDS, vol.25, no.3, pp.288-302, 2012 (Journal Indexed in SCI)

**XXI. Analog Negative-Bias-Temperature-Instability Monitoring Circuit**

Yelten M. B. , Franzon P. D. , Steer M. B.

IEEE TRANSACTIONS ON DEVICE AND MATERIALS RELIABILITY, vol.12, no.1, pp.177-179, 2012 (Journal Indexed in SCI)

**XXII. Demystifying Surrogate Modeling for Circuits and Systems**

Yelten M. B. , Zhu T., KOZIEL S., Franzon P. D. , Steer M. B.

IEEE CIRCUITS AND SYSTEMS MAGAZINE, vol.12, no.1, pp.45-63, 2012 (Journal Indexed in SCI)

**XXIII. Surrogate-Model-Based Analysis of Analog Circuits-Part I: Variability Analysis**

Yelten M. B. , Franzon P. D. , Steer M. B.

IEEE TRANSACTIONS ON DEVICE AND MATERIALS RELIABILITY, vol.11, no.3, pp.466-473, 2011 (Journal Indexed in SCI)

**XXIV. Surrogate-Model-Based Analysis of Analog Circuits-Part II: Reliability Analysis**

Yelten M. B. , Franzon P. D. , Steer M. B.

IEEE TRANSACTIONS ON DEVICE AND MATERIALS RELIABILITY, vol.11, no.3, pp.458-465, 2011 (Journal Indexed in SCI)

## Articles Published in Other Journals

**I. Cryogenic DC Characteristics of Low Threshold Voltage (V<sub>TH</sub>) n-channel MOSFETs**

YELTEN M. B.

Balkan Journal of Electrical and Computer Engineering, vol.7, pp.362-365, 2019 (Other Refereed National Journals)

## Books & Book Chapters

**I. Modelling Methodologies in Analogue Integrated Circuit Design**

Dündar G. (Editor), Yelten M. B. (Editor)

The Institution of Engineering and Technology (IET), London, 2020

**II. Variation-Aware Circuit Macromodeling and Design Based on Surrogate Models**

Zhu T., YELTEN M. B. , Steer M., Franzon P.

in: Simulation and Modeling Methodologies Technologies and Applications, , Editor, Springer, pp.255-269, 2013

**III. Surrogate-Based Modeling and Optimization**

Zhu T., YELTEN M. B. , Steer M., Franzon P.

in: Model Based Variation Aware Integrated Circuit Design, , Editor, Springer, pp.171-188, 2013

## Refereed Congress / Symposium Publications in Proceedings

**I. A High Performance TIA Design in 40 nm CMOS**

GÜNGÖRDÜ A. D. , DÜNDAR G., YELTEN M. B.

2020 IEEE International Symposium on Circuits and Systems (ISCAS), Sevilla, Spain, 12 - 14 October 2020

**II. Design of an Analog Circuit-Based Artificial Neural Network**

Gencer F. B. , Xhafa X., İnam B. B. , YELTEN M. B.

11th International Conference on Electrical and Electronics Engineering, Bursa, Turkey, 28 - 30 November 2019

**III. An Offset Cancellation Set-up for Amplifiers Subject to Aging**

Erol D., GÜNGÖRDÜ A. D. , DÜNDAR G., YELTEN M. B.

11th International Conference on Electrical and Electronics Engineering, 28 - 30 November 2019

- IV. **A Cryogenic CMOS Low Dropout Voltage Regulator Design for Space Applications**  
Kayhan H. İ. , Kabaoğlu A., YELTEN M. B.  
11th International Conference on Electrical and Electronics Engineering, Bursa, Turkey, 28 - 30 November 2019
- V. **Design of a LC-based Voltage-Controlled Oscillator for Space Applications in C - Band**  
Uzun Y., Kabaoğlu A., YELTEN M. B.  
11th International Conference on Electrical and Electronics Engineering, 28 - 30 November 2019
- VI. **Comparison of ELTs with different shapes and a regular layout transistor in 180 nm CMOS process**  
İLİK S., ŞAHİN N., Kabaoğlu A., YELTEN M. B.  
2019 16th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD), Lausanne, Switzerland, Switzerland, 15 - 18 July 2019, pp.21-24
- VII. **A Cryogenic LC VCO Utilizing Cryogenic Models of Active Devices**  
GÜLEÇ H. O. , YELTEN M. B.  
2019 6th International Conference on Electrical and Electronics Engineering (ICEEE), İstanbul, Turkey, 16 - 17 April 2019, pp.220-224
- VIII. **Hysteretic Buck-Boost Converter for Wearable Applications**  
Ozanoğlu K., Cavallini P., YELTEN M. B. , DÜNDAR G.  
2019 15th Conference on Ph.D Research in Microelectronics and Electronics (PRIME), Lausanne, Switzerland, Switzerland, 15 - 18 July 2019, pp.45-48
- IX. **Elyaf Bazlı Elektronik Tekstilde Veri Aktarımı**  
Bayat İ. H. , İLİK S., GÜRARSLAN A., YELTEN M. B.  
Elektrik-Elektronik ve Biyomedikal Mühendisliği Konferansı (ELECO 2018), Bursa, Turkey, 30 November - 01 December 2018
- X. **Design of a LC Voltage-Controlled Oscillator for Space Applications in C-Band**  
Uzun Y., Kabaoglu A., Yelten M. B.  
11th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 28 - 30 November 2019, pp.388-391
- XI. **Yüksek Verimli Geniş Bant J Sınıfı Güç Kuvvetlendiricisi Tasarımı**  
Çağdaş E., Kızılbey O., YAZGI M., PALAMUTÇUOĞULLARI O., YELTEN M. B.  
ELECO 2018, Elektrik-Elektronik ve Biyomedikal Mühendisliği Konferansı, Bursa, Turkey, 30 November - 01 December 2018
- XII. **Impact of transistor scaling on the time-dependent dielectric breakdown (TDDB) reliability of analog circuits**  
SANIÇ M. T. , YELTEN M. B.  
2017 10th International Conference on Electrical and Electronics Engineering (ELECO), 30 November 2017 - 02 December 2018
- XIII. **Elyaf Bazlı Elektronik Tekstillerde Veri Aktarımı**  
BAYAT H. İ. , İLİK S., GÜRARSLAN A., YELTEN M. B.  
Elektrik Elektronik ve Biomühendislik Konferansı, Bursa, Turkey, 30 November - 01 December 2018
- XIV. **A High Speed 180 NM CMOS Cryogenic SAR ADC**  
SUNA A., ÇEVİK İ., YELTEN M. B.  
2018 18th Mediterranean Microwave Symposium (MMS), İstanbul, Turkey, 31 October - 02 November 2018
- XV. **Cryogenic Measurement of Low Threshold Voltage MOSFETs**  
YELTEN M. B.  
International Congress on Semiconductor Material and Devices, Ardahan, Turkey, 28 - 30 August 2018
- XVI. **Design of Logic Gates by Using a Four-Gate Thin Film Transistor (FG TFT)**  
İik S., GENCER F. B. , Yelten M. B.  
15th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, SMACD 2018, Prague, Czech Republic, 2 - 05 July 2018, pp.77-80
- XVII. **A Novel Multiple Membership Function Generator for Fuzzy Logic Systems**  
KHAYATZADEH R., Yelten M. B.  
15th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit

- Design, SMACD 2018, Prague, Czech Republic, 2 - 05 July 2018, pp.101-104
- XVIII. A rare event based yield estimation methodology for analog circuits**  
ODABAŞI I. Ç. , Yelten M. B. , AFACAN E., Baskaya F., Pusane A. E. , Dündar G.  
21st IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems, DDECS 2018, Budapest, Hungary, 25 - 27 April 2018, pp.33-38
- XIX. A test-bed based guideline for multi-source energy harvesting**  
DAVUT E., KAZANCI O., ÇAĞLAR A., ALTINEL D., YELTEN M. B. , KARABULUT KURT G. Z.  
2017 10th International Conference on Electrical and Electronics Engineering (ELECO), Bursa, Turkey, 30 November - 02 December 2017, pp.1-4
- XX. A 0.18  $\mu$ m CMOS X-BAND Low Noise Amplifier for Space Applications**  
Sahin N., Yelten M. B.  
1st New Generation of CAS Conference (NGCAS ), Genoa, Italy, 6 - 09 September 2017, pp.205-208
- XXI. A high linearity LNA using 180 nm CMOS technology for S-Band**  
Çağlar A., Berke Yelten M. B.  
2017 European Conference on Circuit Theory and Design, ECCTD 2017, Catania, Italy, 4 - 06 September 2017
- XXII. Reliability of 3D-printed dynamic scanners**  
GÖNÜLTAŞ B. M. , AYGÜN S., KHAYATZADEH R., Çivitci F., Gökdel Y. D. , Yelten M. B. , Ferhanoğlu O.  
22nd IEEE International Mixed-Signals Test Workshop, IMSTW 2017, Thessaloniki, Greece, 3 - 05 July 2017
- XXIII. A cryogenic modeling methodology of MOSFET I-V characteristics in BSIM3**  
KABAOĞLU A., Berke Yelten M. B.  
14th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, SMACD 2017, Giardini Naxos, Taormina, Italy, 12 - 15 June 2017
- XXIV. Review: Analog design methodologies for reliability in nanoscale CMOS circuits**  
Afacan E., Berke Yelten M. B. , Dundar G.  
14th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, SMACD 2017, Giardini Naxos, Taormina, Italy, 12 - 15 June 2017
- XXV. A Heuristic Sensitivity Analysis Technique for High-Dimensional Systems**  
Yelten M. B.  
23rd IEEE International Conference on Electronics, Circuits and Systems (ICECS), Monaco, 11 - 14 December 2016, pp.181-184
- XXVI. A variability analysis algorithm for spiral inductors using enhanced single- $\pi$  model**  
SOYSAL I. B. , AKSO E., YELTEN M. B.  
2016 National Conference on Electrical, Electronics and Biomedical Engineering (ELECO), Bursa, Turkey, 1 - 03 December 2016
- XXVII. A Variability Analysis Algorithm for Spiral Inductors Using Enhanced Single- $\pi$  Model**  
Soysa I. B. , Akso E., Yelten M. B.  
National Conference on Electrical, Electronics and Biomedical Engineering (ELECO), Bursa, Turkey, 1 - 03 December 2016, pp.442-446
- XXVIII. Efficient signature selection tool for sense & react systems**  
Afacan E., Dündar G., Pusane A. E. , Başkaya F., Yelten M. B.  
13th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, SMACD 2016, Lisbon, Portugal, 27 - 30 June 2016
- XXIX. Scalable and Efficient Analog Parametric Fault Identification**  
Yelten M. B. , NATARAJAN S., XUE B., GOTETİ P.  
32nd IEEE/ACM International Conference on Computer-Aided Design (ICCAD), San-Jose, Costa Rica, 18 - 21 November 2013, pp.387-392
- XXX. Process Mismatch Analysis based on Reduced-Order Models**  
Yelten M. B. , Franzon P. D. , Steer M. B.  
13th International Symposium on Quality Electronic Design (ISQED), Santa-Clara, Cuba, 19 - 21 March 2012, pp.648-655
- XXXI. Theoretical Analysis and Characterization of the Tunable Matching Networks in Low Noise**

## **Amplifiers**

Yelten M. B. , Gard K. G.

European Conference on Circuit Theory Design, Antalya, Turkey, 23 - 27 August 2009, pp.890-893

### **XXXII. A novel design procedure for tunable low noise amplifiers**

Yelten M. B. , Gard K. G.

2009 IEEE 10th Annual Wireless and Microwave Technology Conference, WAMICON 2009, Clearwater, FL, United States Of America, 20 - 21 April 2009

## **Supported Projects**

Yelten M. B. , YAVUZ Y., ÇAKMAK C., KARADEMİR J., PEKER E., KORKMAZ F. S. , Project Supported by Higher Education Institutions, Experimental Determination of Gate-Oxide Breakdown of CMOS Transistors at Cryogenic Temperatures, 2020 - Continues

Yelten M. B. , Erol D., Project Supported by Higher Education Institutions, A Low-Power Low Noise Amplifier Design in 40 nm CMOS Technology, 2019 - Continues

Akgül T., Ferhanoglu O., Yelten M. B. , Project Supported by Higher Education Institutions, Yüksek Hızlı Kamera Görüntülerinin Elde Edilmesi ve Analizi, 2018 - Continues

Yelten M. B. , Karabulut Kurt G. Z. , Güngördü A. D. , Project Supported by Higher Education Institutions, Kablosuz Enerji Hasatlaması İçin Tümlşik Devre Çözümleri, 2018 - Continues

Yelten M. B. , TUBITAK Project, Uzay Araştırmaları İçin Analog Ve Radyo Frekansı Metal-Oksit-Yarıiletken-Alan-Etkili-Transistör (Mosfet) Modellemesi Ve Tümlşik Devre Tasarımı, 2016 - Continues

Yelten M. B. , Rivera A., EU Supported Other Project, CA17126 - Towards understanding and modelling intense electronic excitation, 2018 - 2022

Yelten M. B. , TUBITAK Project, Nesnelerin İnterneti, Savunma ve Yenilikçi Hesaplama Yöntemlerindeki Analog Devre Uygulamaları için 40 nm Transistör Teknolojisinin Zamana Bağlı Yıpranma, Kriyojenik Ortam ve Işınım Etkilerine Yönelik Ölçeklendirilebilir Modellemesi, 2018 - 2021

Ferhanoglu O., Yelten M. B. , TUBITAK Project, Lazer Taramalı Endoskoplara Yönelik Hibrit Uyarma Mekanizmalı ve Yekpare 3-Boyutlu Tarayıcı Sistemi, 2018 - 2020

Yelten M. B. , Dabak S., TUBITAK Project, 320x240 Piksel RGB AMOLED Aviyonik Ekran Geliştirilmesi, 2015 - 2020

Yelten M. B. , İlik S., Project Supported by Higher Education Institutions, Metal-Oksit-Yarıiletken-Alan-Etkili-Transistörlerinde Işınım Etkilerinin İncelenmesi ve Modellenmesi, 2017 - 2019

Yelten M. B. , TUBITAK Project, Uzay Araştırmaları İçin Analog Ve Radyo Frekansı Metal Oksit Yarıiletken Alan Etkili Transistör Mosfet Modellemesi Ve Tümlşik Devre Tasarımı, 2016 - 2019

Yelten M. B. , Project Supported by Higher Education Institutions, Yonga Üstü Tümlşik İndükleçlerin Modellenmesi, 2016 - 2018

Yelten M. B. , Karalar T. C. , TUBITAK Project, SiGe BiCMOS proses ile 12bit ve 2Gbps hızında bir Analog Dijital Çevirici Analog Digital Converter tasarlanması, 2015 - 2018

Yelten M. B. , Project Supported by Higher Education Institutions, Metal-Oksit-Yarıiletken-Alan-Etkili-Transistörlerinde (MOSFET lerde) Kriyojenik Sıcaklık, Işınım, Zamana Bağlı Yıpranma ve Proses Değişkenliği Etkilerinin Kapsamlı Modellenmesi, 2016 - 2017

Yelten M. B. , Project Supported by Higher Education Institutions, Metal-Oksit-Yarıiletken-Alan-Etkili-Transistörlerinde (MOSFET'lerde) Kriyojenik Sıcaklık, Işınım, Zamana Bağlı Yıpranma ve Proses Değişkenliği Etkilerinin Kapsamlı Modellenmesi, 2016 - 2017

Yelten M. B. , TUBITAK Project, Çok Boyutlu Sistemler İçin Çevrimiçi Öğrenmeye Dayalı Hassasiyet Analizi, 2016 - 2017

## **Edit Congress and Symposium Activities**

ELECO 2019, Moderator, Bursa, Turkey, 2019

SMACD 2019, Moderator, Lausanne, Switzerland, 2019

ICEEE 2019, Working Group, İstanbul, Turkey, 2019  
ELECO 2018, Working Group, Bursa, Turkey, 2018  
MMS 2018, Moderator, İstanbul, Turkey, 2018  
SMACD 2018, Attendee, Praha, Czech Republic, 2018  
DDECS 2018, Attendee, Budapest, Hungary, 2018  
ELECO 2017, Attendee, Bursa, Turkey, 2017  
ICSMD 2018, Attendee, Ardahan, Turkey, 2017  
SMACD 2017, Attendee, Catania, Italy, 2017  
ELECO 2016, Attendee, Bursa, Turkey, 2016  
ICECS 2016, Attendee, Monte Carlo, Monaco, 2016

## **Citations**

Total Citations (WOS):190

h-index (WOS):6

## **Non Academic Experience**

Business Organization (private), Intel Corporation